## **REMARKS**

At the outset, the Examiner is thanked for the thorough review and consideration of the pending application. The final Office Action dated March 7, 2006, has been received and its contents carefully reviewed.

Claims 1-3 and 6-8 are rejected and claims 4, 5, 9 and 10 are objected to by the Examiner. Claims 1, 7, and 10 have been amended, claim 4 has been canceled. Claims 1-10 remain pending in this application.

In the Office Action, claims 1-3 and 6-8 are rejected under 35 U.S.C. § 103(a) as being unpatentable over U.S. Patent No. 5,486,868 to Shyu et al. (hereinafter "Shyu") in view of U.S. Patent No. 6,873,312 to Matsueda (hereinafter "Matsueda").

The rejection of claims 1-3 and 6-8 is respectfully traversed and reconsideration is requested. Claims 1-3 and 6 are allowable over the cited references in that each of these claims recites a combination of elements including, for example, "a third controller for generating a signal informing a sampling start of a data and a source sampling clock for latching a data at the rising or falling edge during one horizontal synchronization period, a forth controller for deforming a gate output enable signal generated from the first controller by making the gate output enable signal into a high state during a certain time so as to prevent a latch-up badness in which all the outputs of a gate drive integrate circuit goes to a high state, thereby disabling the gate drive integrated circuit, and a fifth controller for always equally keeping the polarity of the horizontal/vertical synchronizing signal." Neither Shyu nor Matsueda, singly or in combination, teach or suggest at least this feature of the claimed invention. The Examiner in the Office Action admits these features of the claims are not present in Shyu and Matsueda, as amended claim 1 now included the allowable subject matter of claim 4. Accordingly, Applicant respectfully submits that claims 1-3 and 6 are allowable over the cited references.

Claims 7 and 8 are allowable over the cited references in that each of these claims recites a combination of elements including, for example, "wherein the first controller includes: a first counter for receiving a horizontal synchronizing signal and the first timing information inputted from the decoder to count the timing information during two horizontal periods and thus output a first count value; a subtractor for subtracting the timing information from the first count value to output a reference timing signal; a second counter for counting the timing information every

period of the horizontal synchronizing signal to output a second count value for the current horizontal period; a first comparator for comparing the second count value with the reference timing signal to output a first selection timing signal; a third counter for receiving the first selection timing signal as an initializing signal to count the reference clock during one horizontal period and thus output a third count value; a second comparator for receiving the third count value to compare it with a second timing information inputted from the decoder, thereby outputting a second selection timing signal when the two input values are equal." Neither Shyu nor Matsueda, singly or in combination, teach or suggest at least this feature of the claimed invention. The Examiner in the Office Action admits these features of the claims are not present in Shyu and Matsueda. Accordingly, Applicant respectfully submits that claims 7 and 8 are allowable over the cited references.

Applicants believe the foregoing amendments place the application in condition for allowance and early, favorable action is respectfully solicited.

If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at (202) 496-7500 to discuss the steps necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. § 1.136, and any additional fees required under 37 C.F.R. § 1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

Dated: June 7, 2006

Registration No. 40,106

McKENNA LONG & ALDRIDGE LLP 1900 K Street, N.W. Washington, DC 20006 (202) 496-7500 Attorneys for Applicant

 $+ + 4_i$